



#5A/2-22-04
JAN 29 2004
2133

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Peter James Sutherland Goldsack) Group Art No.: 2133
Application No: 09/875,415) Examiner: Torres, Joseph D.
Filed: 6/05/2001) Re: RESPONSE
For: "Block Error Rate Measurements") Our Ref: B-4199 618841-6
) Date: January 26, 2004

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FEB 02 2004

Technology Center 2100

Commissioner for Patents
POB 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is filed in response to the official action dated October 24, 2003.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.

